Tshwane University of Technology
We empower people

PART-TIME LECTURERS x14
DEPARTMENT OF COMPUTER SYSTEMS ENGINEERING
FACULTY OF ICT
SOSHANGUVE SOUTH CAMPUS
Ref: 26/G20

The Department of Computer Systems Engineering, Faculty of ICT, invites applications for 14 Undergraduate Part–time Lecturer positions at the Soshanguve South Campus.

Diploma level

- DE1F05D Digital Electronics 115 (Extended) (1 position)
- EL1F05D Electronics 115 (1 position)
- PCOF05D Project Construction (1 position)
- DEL216D Digital Electronics 216 (2 positions)
- DP1216D Digital Process Control 216 (1 position)
- PD1315D Project Design 365 (1 position)
- EMS316D Embedded Systems 316
- CAO216D Computer Architecture and Organisation (4 Positions)
- CFA115D Computing Fundamentals A (2 positions)

NB: Indicate the module(s) you are applying for.

INSTRUCTIONS: Applicants are required to submit, together with the CVs, a fully completed standardized application form available from the university intranet as well as recently certified copies of qualifications and Identity Document. Failure to comply with this instruction will disqualify the candidate. Candidates may be subjected to appropriate psychometric testing and other selection instruments.

Key performance areas/Core functions:

- Preparing all lecture materials, assessments, and related documents
- Presenting lectures and assessing students in the department
- Performing administration of marks and other related administrative duties under the subject head, sectional head and HOD supervision.
- Collate and submit complete course files (including lesson plans, assessments, moderation reports, and student performance records) before and after each semester.

Women and people with disabilities are encouraged to apply.

- Integrate ECSA Graduate Attributes into curriculum delivery, assessment design, and student development.
- Participate in ECSA accreditation preparation, including evidence collection, documentation, and quality assurance activities.
- Undertake other related academic or administrative tasks as assigned by the HOD.
- The appointments are for the TUT contact period for 1st semester of 2026.
- Successful applicants only receive a part-time salary according to TUT policy.
- No additional remuneration for traveling, relocation, etc.

Minimum requirements:

1. Academic qualifications:
- Master's in Computer Systems Engineering, Electrical Engineering, and Mechanical Engineering with adequate knowledge in the subjects.
  PLEASE NOTE: if enough suitable candidates with a master's cannot be found, candidates with PGDip/Honours and B-Tech and will be considered.

2. Experience:
- 3 years relevant teaching experience and/or 3 years' experience in industry.

3. Additional Requirements:
- Must attend the academic orientation programme designed for a part time lecturer.
- Must attend Departmental Curriculum review.

3. Registration with a professional body (if applicable):
- Registration with ECSA is recommended.

5. Postgraduate Students:
- Supervisors Progress report should be attached together with the application.

Send CV, application form, academic transcripts, and qualifications to:
https://docs.google.com/forms/d/e/1FAIpQLSf5hCK2AUKnNss9HZwCEbA0Botwbu5JHIONW4WxCYNU-R2hIw/viewform?usp=header

Closing Date   : 05 February 2026

Enquiries   : Prof C Du DuC@tut.ac.za

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NB: If we have not responded within a month from the closing date, you should regard your application as unsuccessful. Correspondence will only be entered into with short-listed candidates. **The University reserves the right not to make an appointment.** It is the intention of the University to promote representativity in respect of race, gender and disability through the filling of this post.